





SH-J1

SNT-100-BK-G

H1

1

NY PMS 440 0025 PH

H2

1

NY PMS 440 0025 PH

H3

1

NY PMS 440 0025 PH

H4

1

NY PMS 440 0025 PH

H5

Nut

H6

Nut

H7

Nut

H8

Nut

PCB

LOGO

FCC disclaimer

PCB

LOGO

WEEE logo

PCB

LOGO

Texas Instruments

LOGO10

PCB Number: HVP012

PCB Rev: A

LOGO4

DANGER HIGH VOLTAGE

LOGO8

DANGER HIGH VOLTAGE

LOGO7

DANGER HIGH VOLTAGE

LOGO6

DANGER HIGH VOLTAGE

CAUTION HOT SURFACE

CAUTION HOT SURFACE

CAUTION HOT SURFACE

CAUTION HOT SURFACE

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: LMG1210EVM-012

TID #: N/A

Number: HVP012

SVN Rev: Version control disabled

Drawn By:

Engineer: Alberto Doronzo

Designed for: Public Release

Project Title: LMG1210-HB-EVM

Sheet Title:

Assembly Variant: 001

File: HVP012A-hardware.SchDoc

Contact: http://www.ti.com/support

Mod. Date: 6/4/2018

Rev: A

Sheet: 2 of 3

Size: B

TEXAS

INSTRUMENTS

http://www.ti.com

© Texas Instruments 2016

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.